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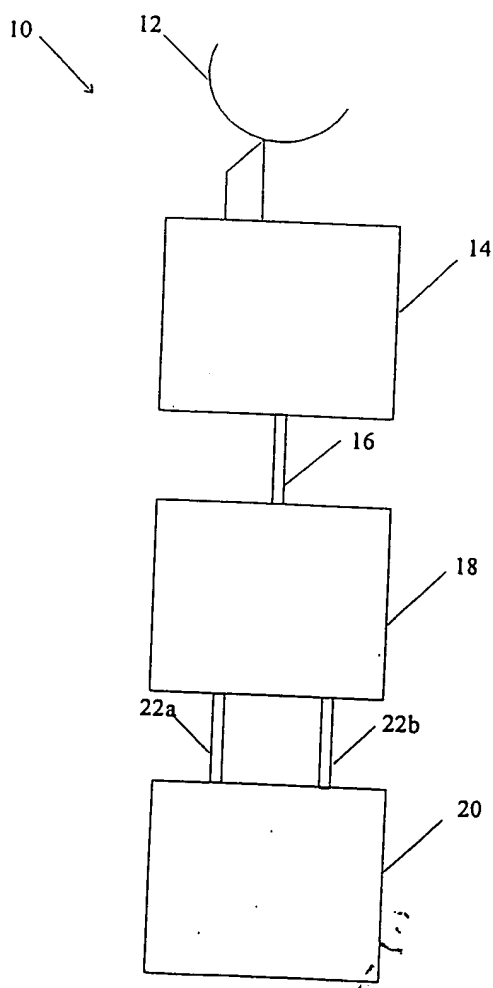


Figure 1

(6)

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Figure 1 is a block diagram of a multi-channel system. It consists of three parallel processing channels, labeled 18a, 18b, and 18c, which share a common output stage.

- Channel 18a:** Receives input 12 through a switch 24. The input is split into two paths: one through a block 26a to a summing junction 30, and another through a block 26b to a summing junction 36. The summing junction 30 also receives feedback from block 32a. The output of 30 goes through block 34a to a summing junction 38a. The output of 38a goes through block 40 to a summing junction 42. The output of 42 is connected to block 44.
- Channel 18b:** Receives input 20 through a switch 21. The input is split into two paths: one through a block 22a to a summing junction 54a, and another through a block 22b to a summing junction 58a. The summing junction 54a also receives feedback from block 52a. The output of 54a goes through block 56a to a summing junction 50a. The output of 50a is connected to block 48.
- Channel 18c:** Receives input 20 through a switch 21. The input is split into two paths: one through a block 22c to a summing junction 54b, and another through a block 22d to a summing junction 58b. The summing junction 54b also receives feedback from block 52b. The output of 54b goes through block 56b to a summing junction 50b. The output of 50b is connected to block 48.

The output stage consists of a summing junction 42, a block 44, and a summing junction 48. The output of 48 is connected to block 44.

Figure 2

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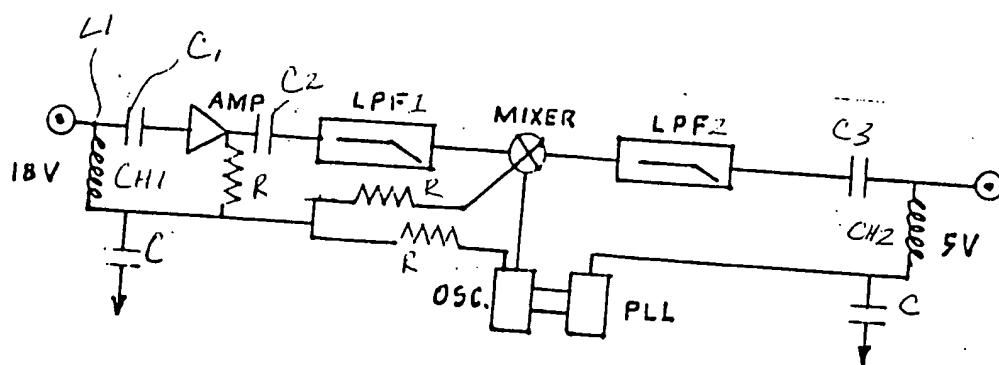


Fig. 3a

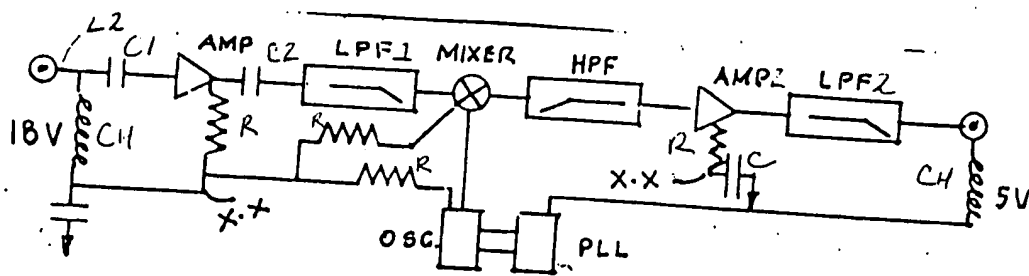
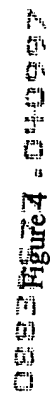
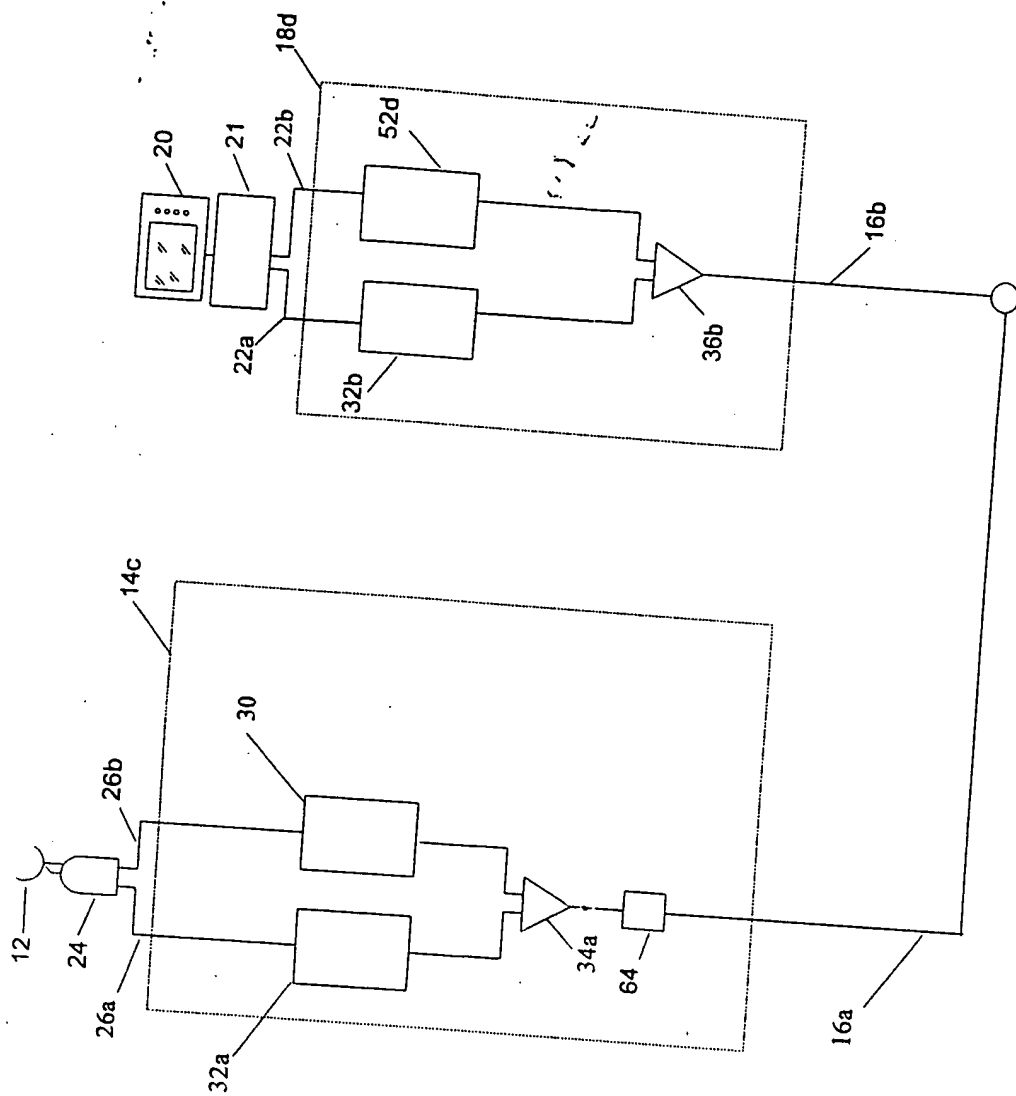


Fig. 3b

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